

100

IOP

IL

R

RST

AXp

PL

AFS { 50b, 50a }

18

20

AS

DS

16

W

FP

24

26

22

MAIN CONTROLLER

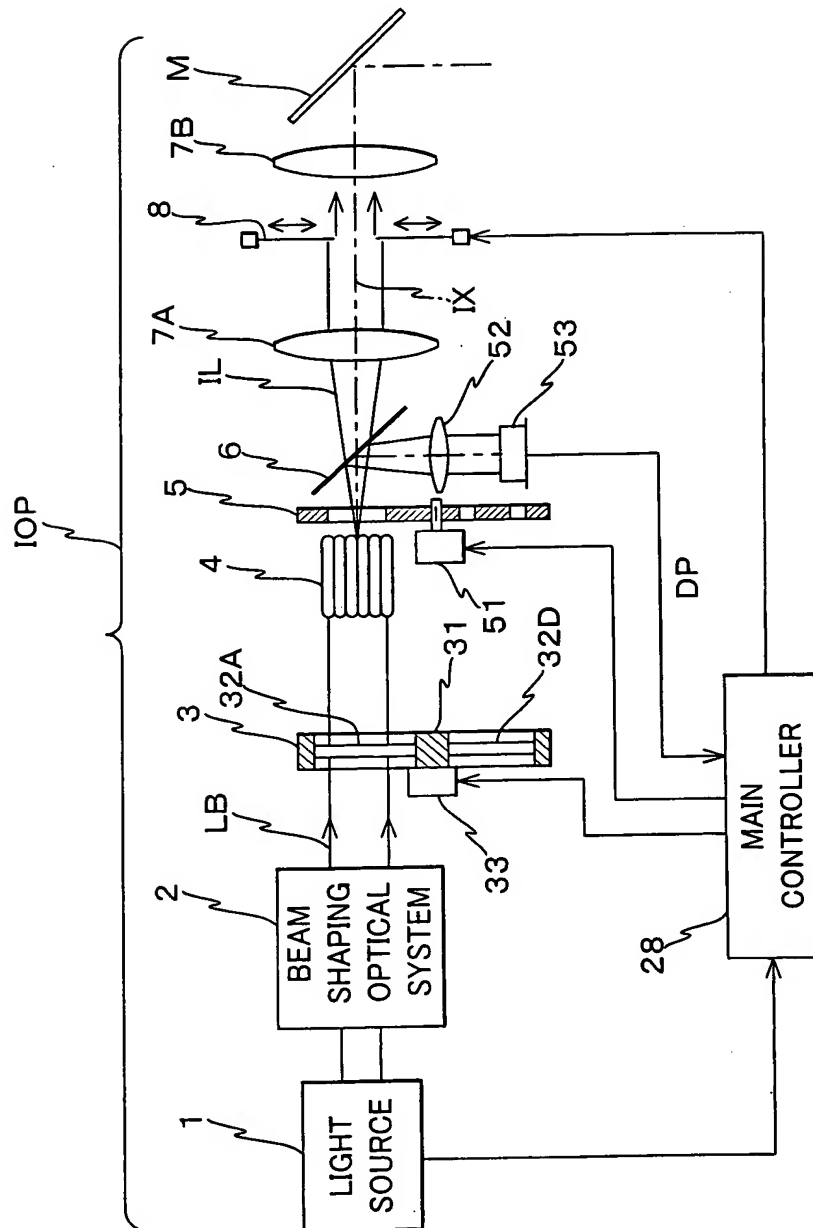
28

Z

X

Y

Fig. 2



*Fig. 3*

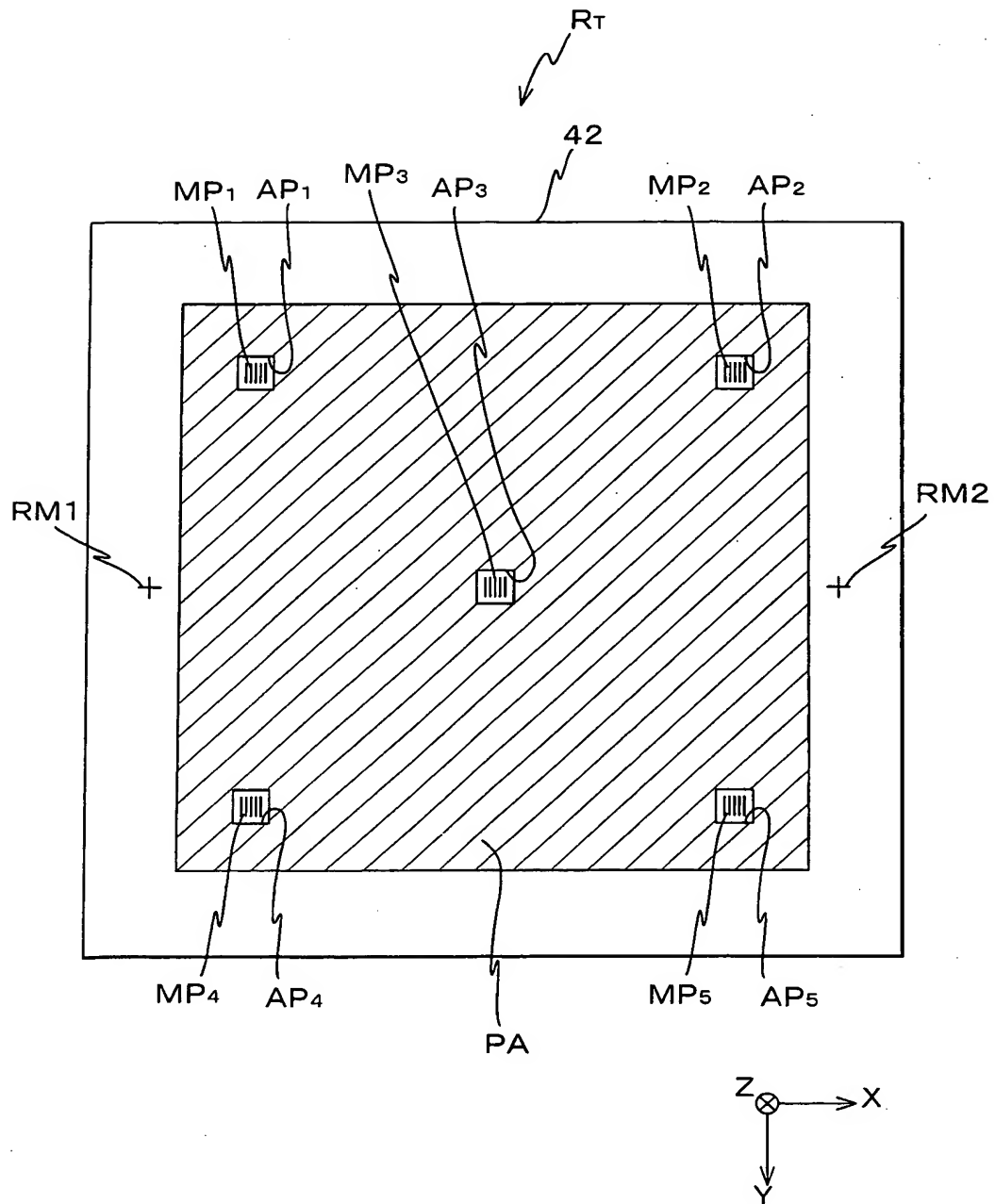


Fig. 4

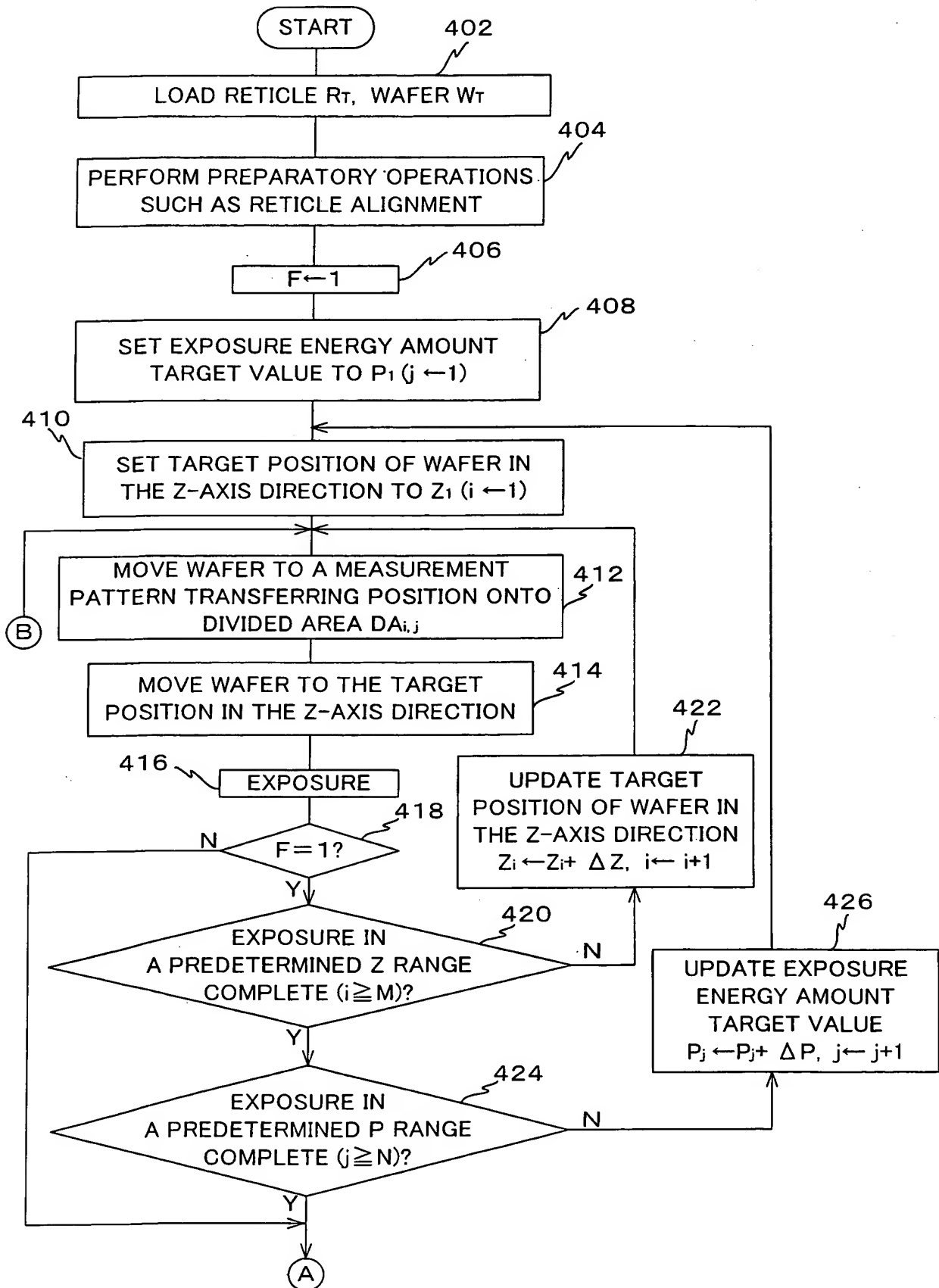
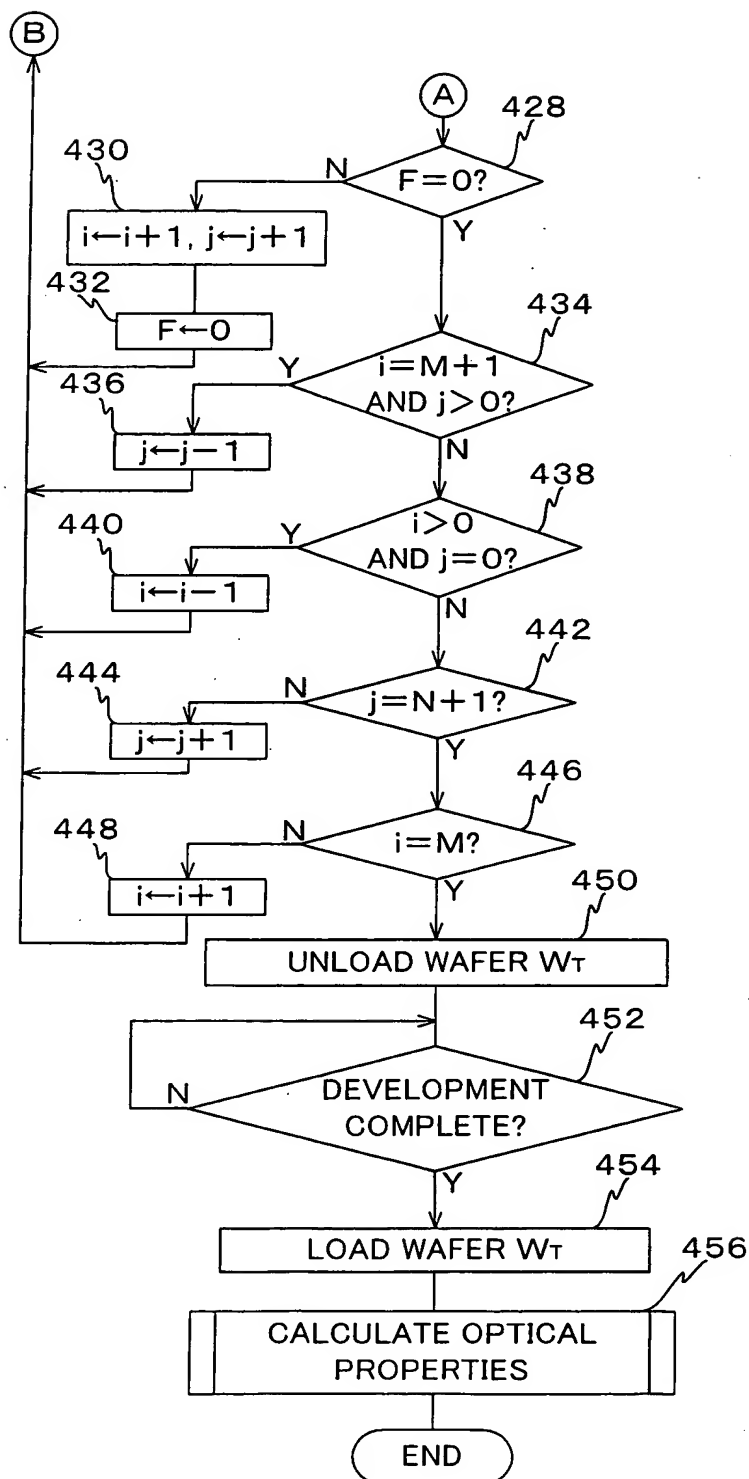
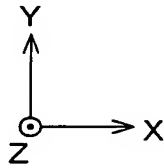
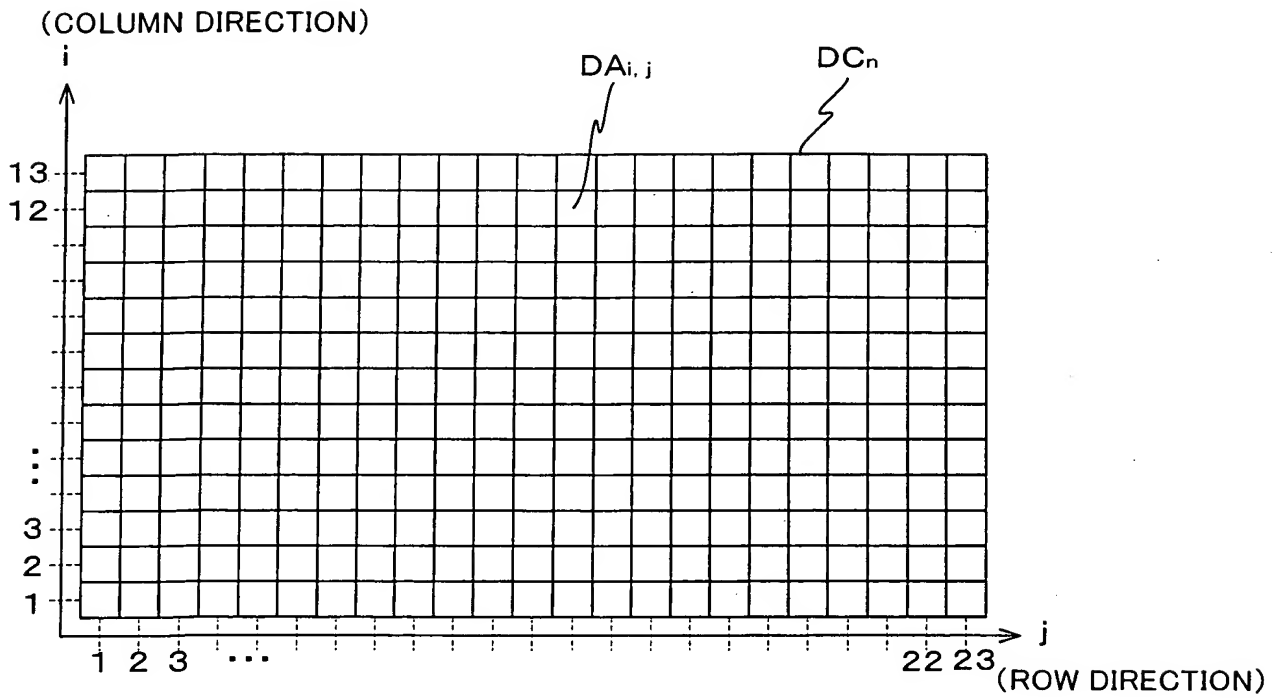


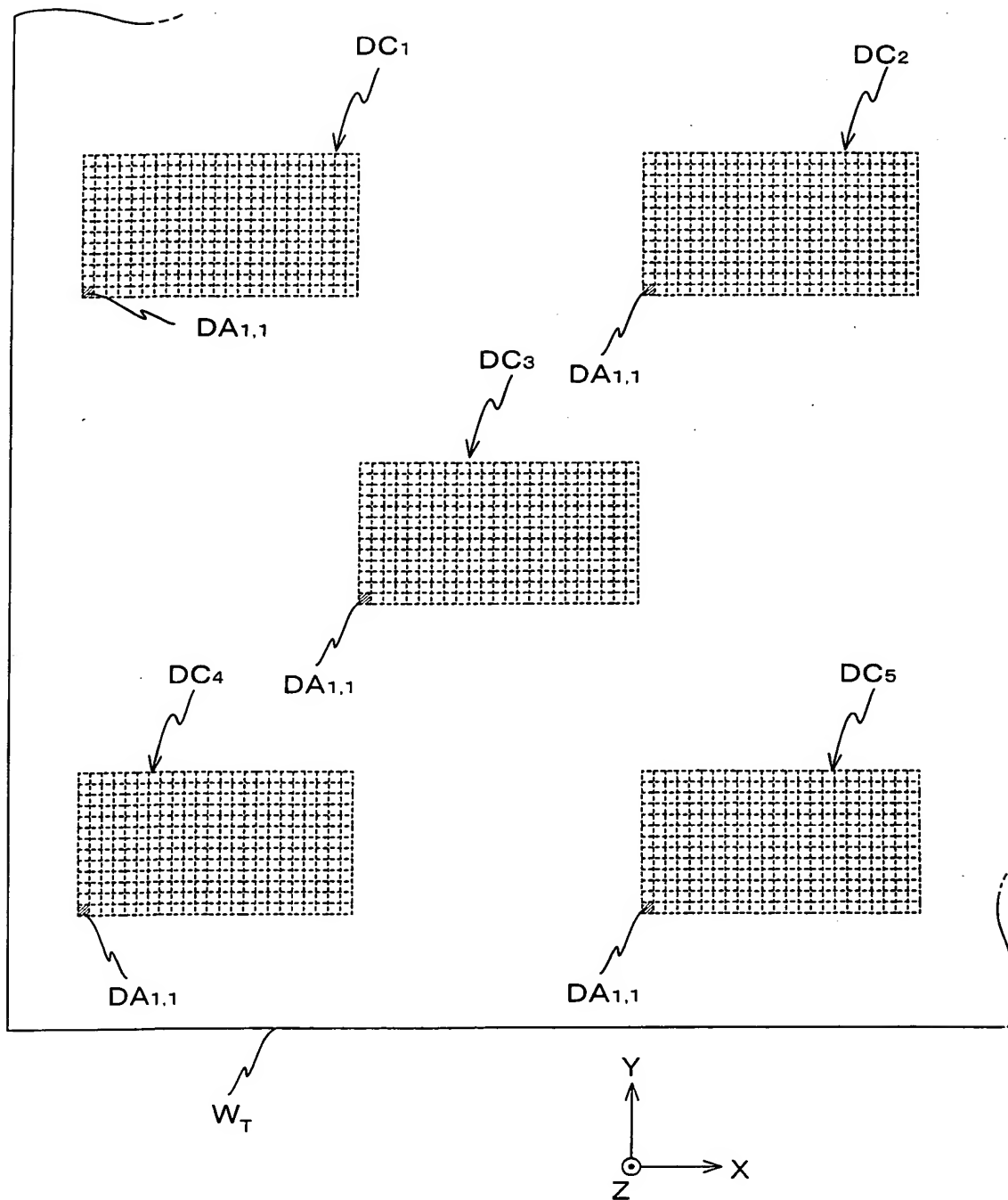
Fig. 5



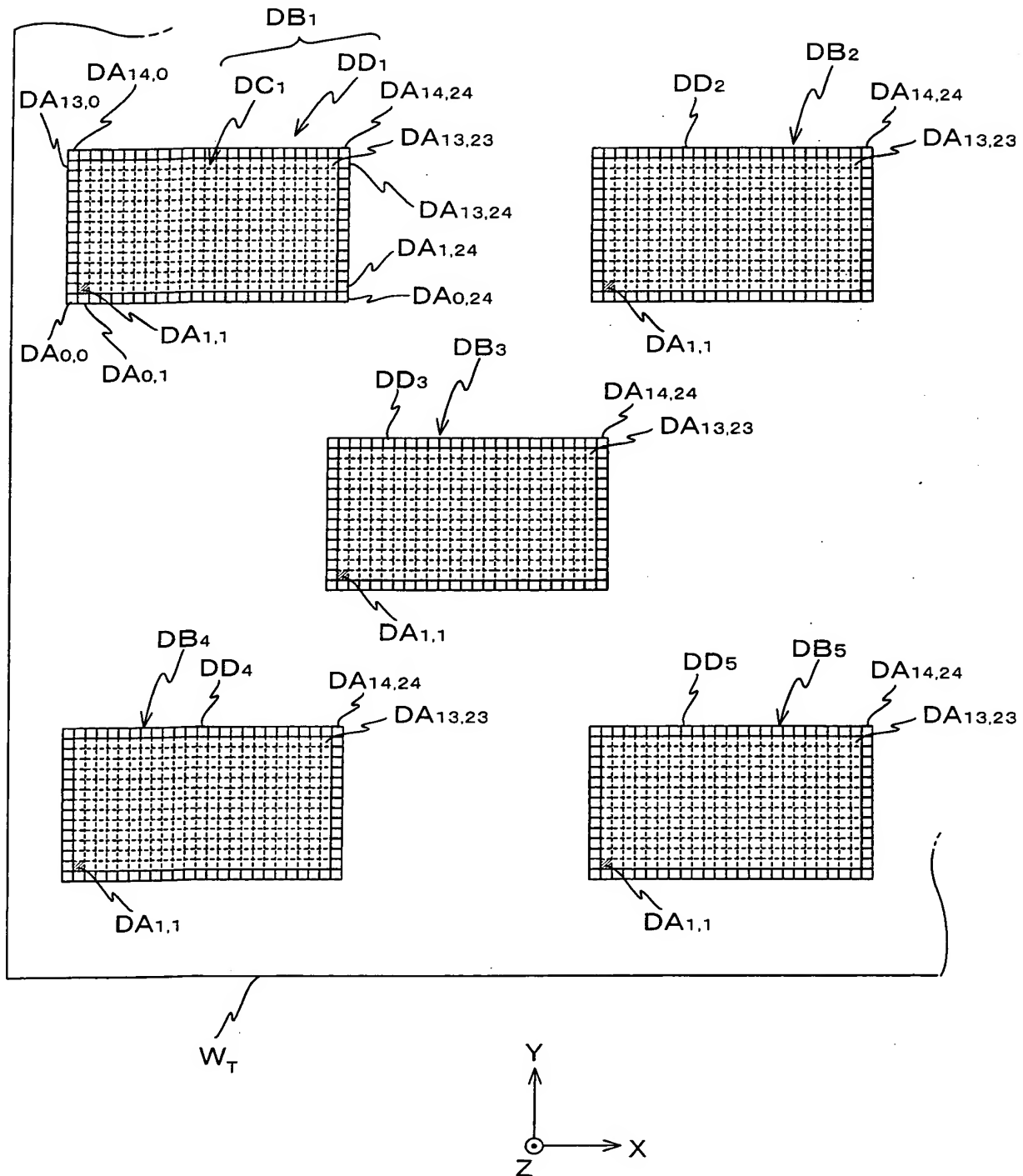
*Fig. 6*



*Fig. 7*

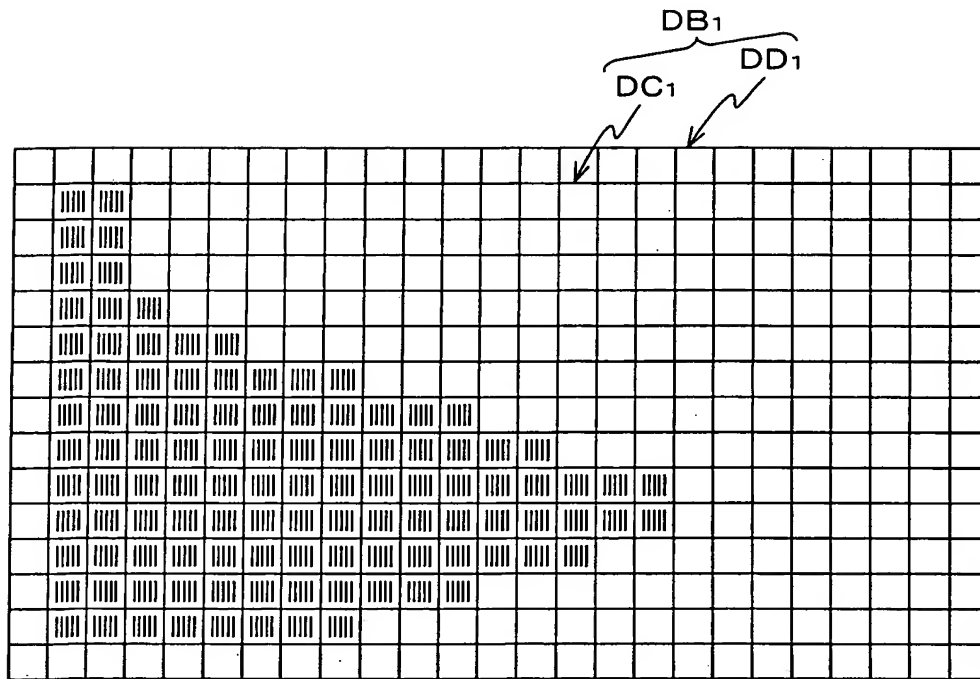


*Fig. 8*

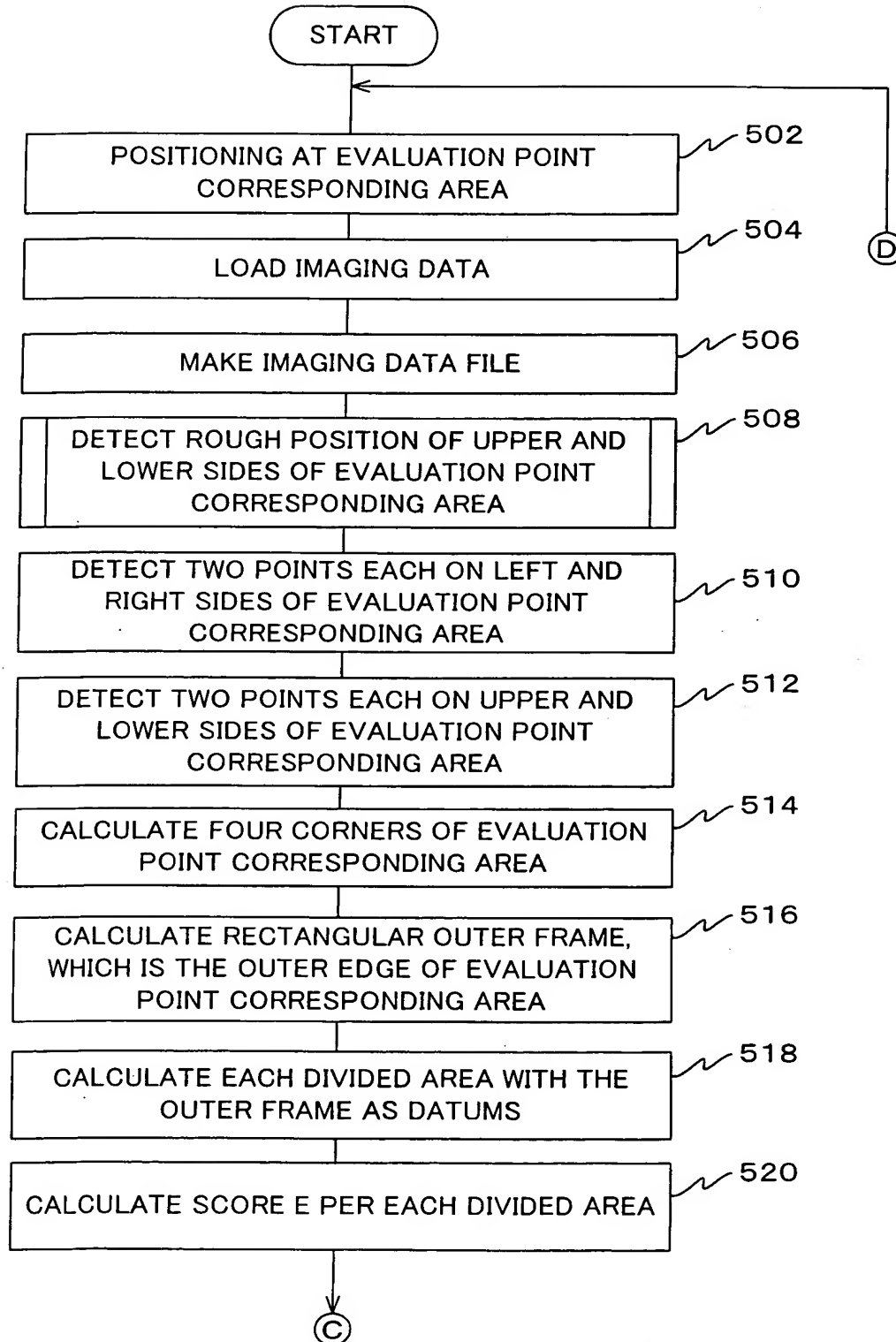




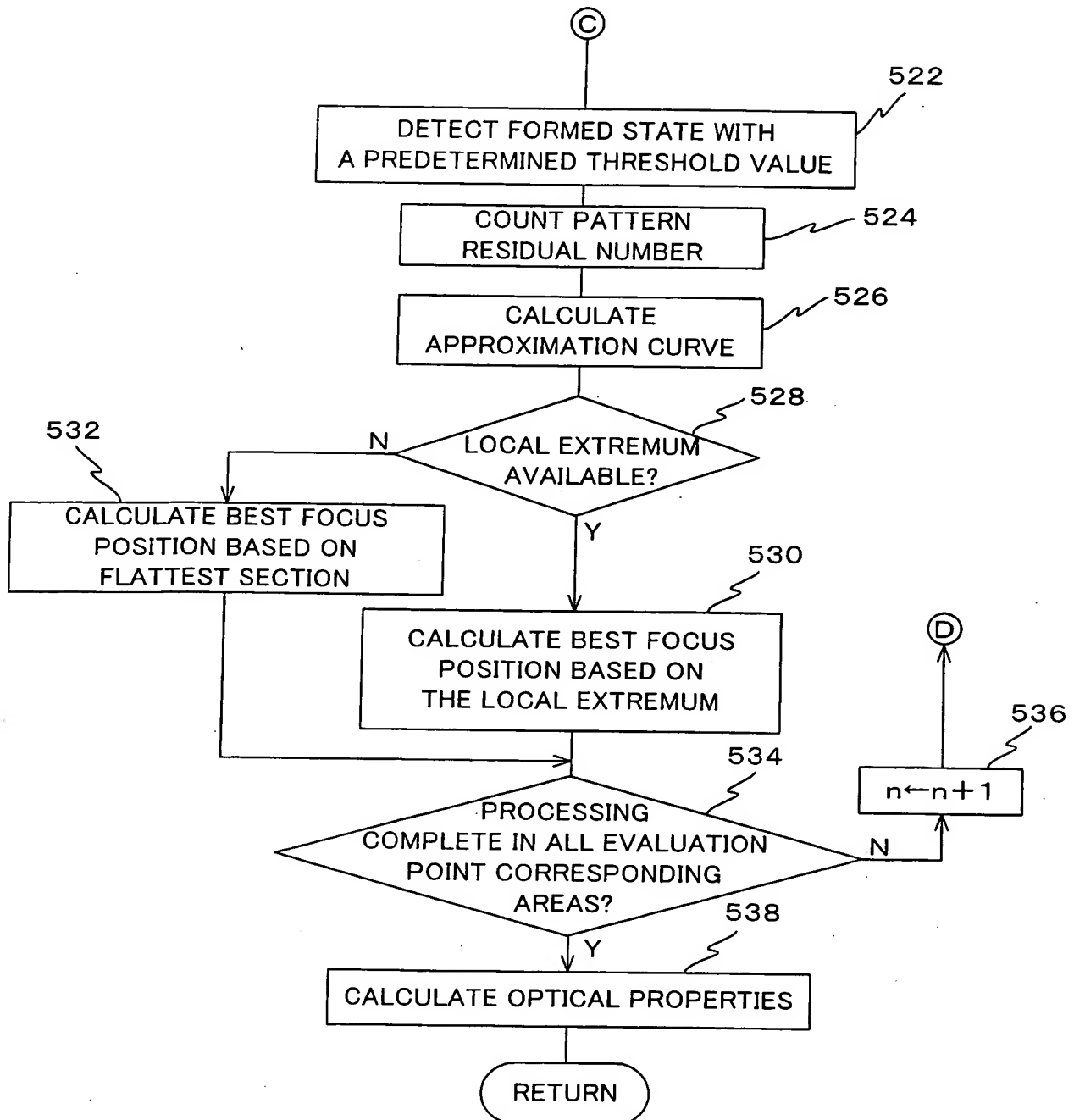
*Fig. 9*



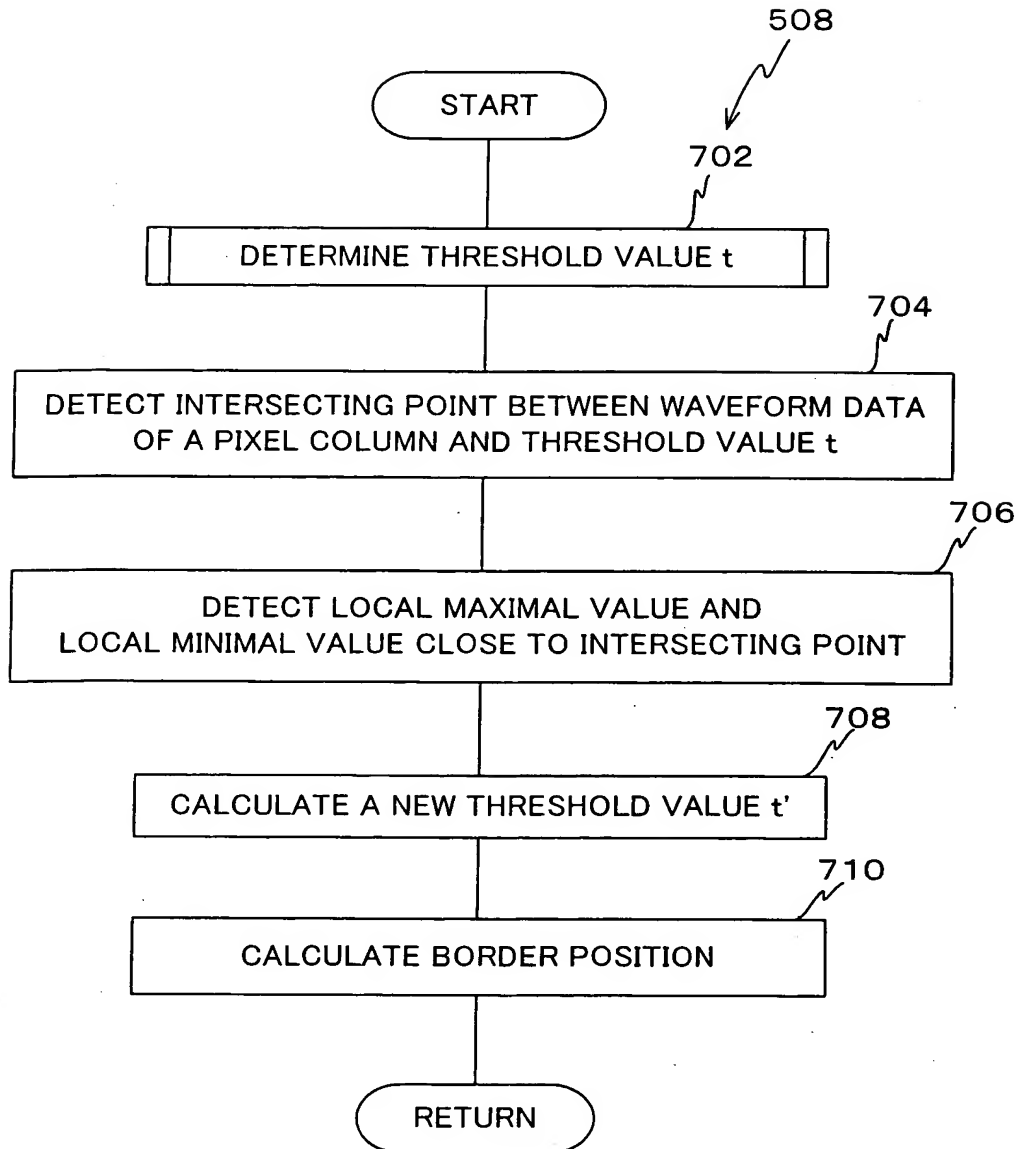
*Fig. 10*



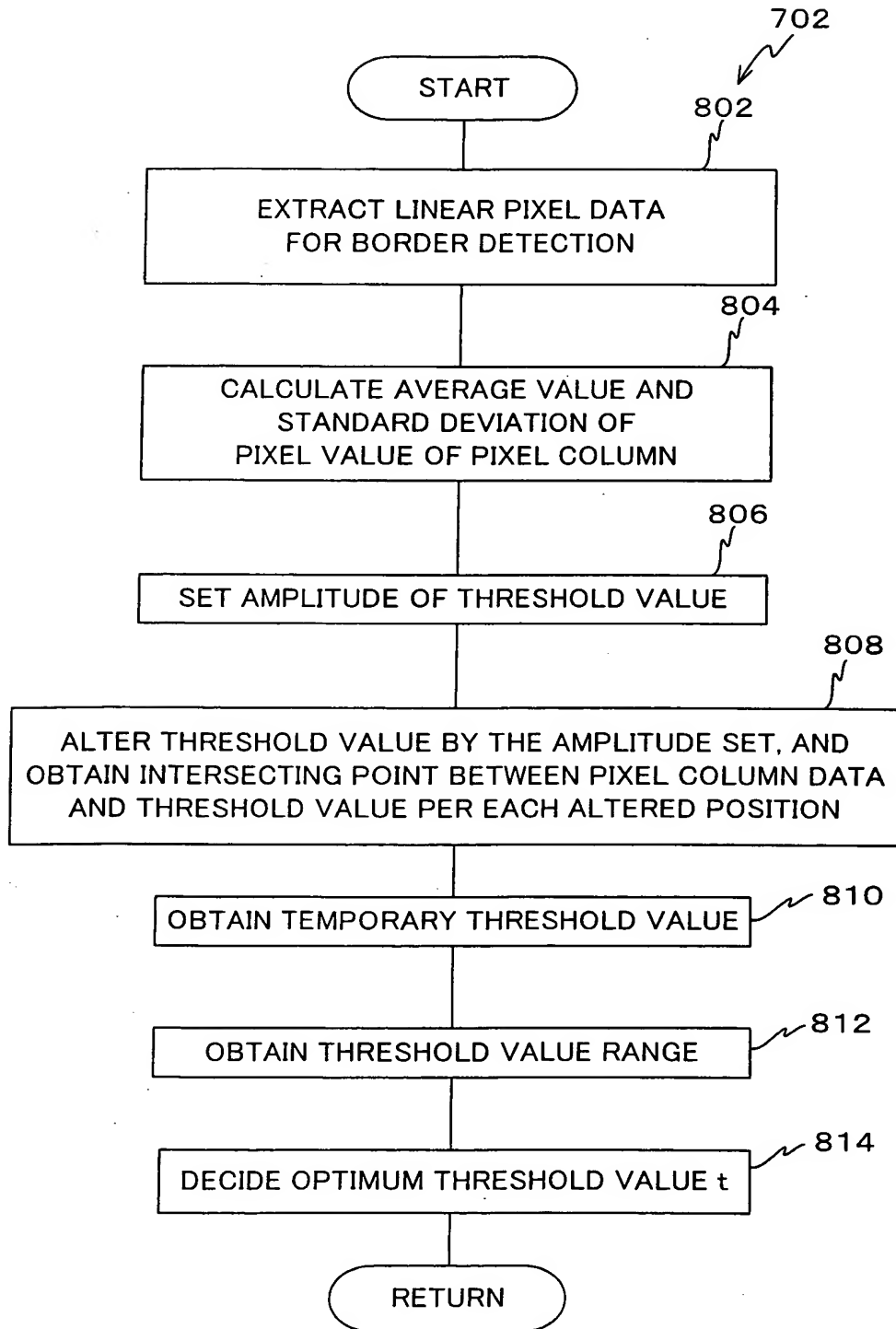
*Fig. 11*



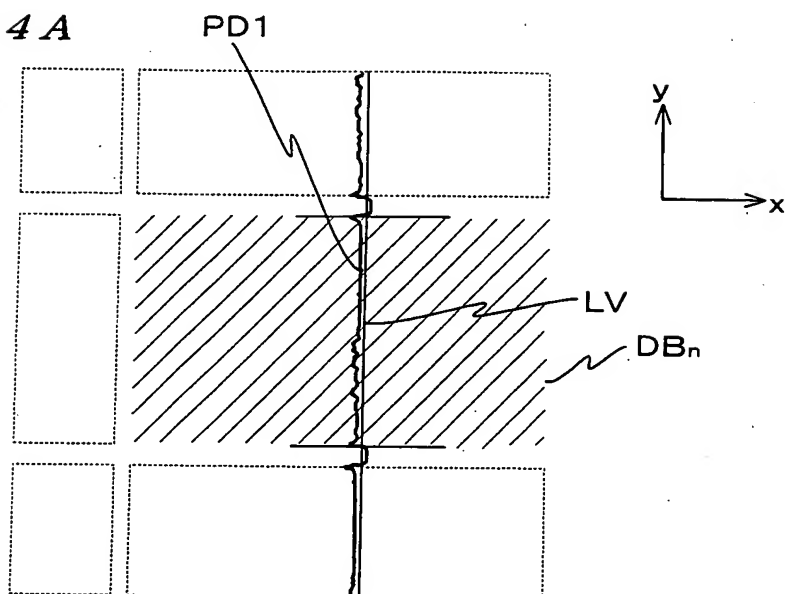
*Fig. 12*



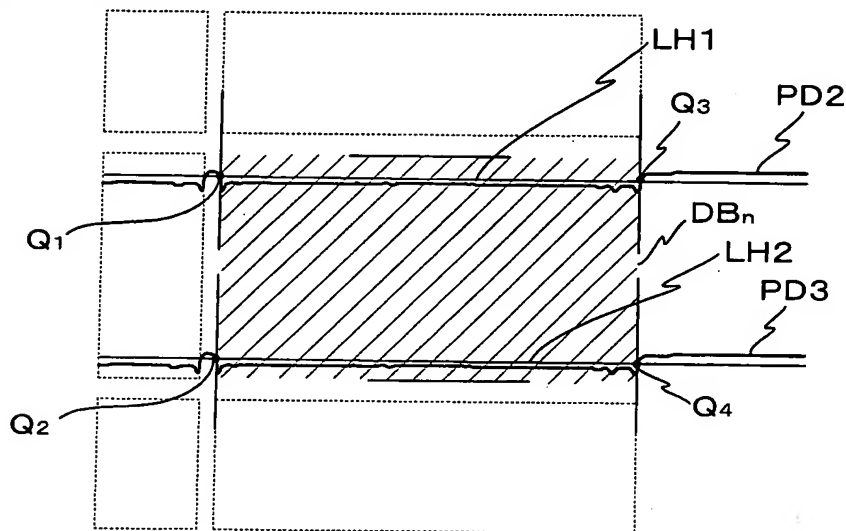
*Fig. 13*



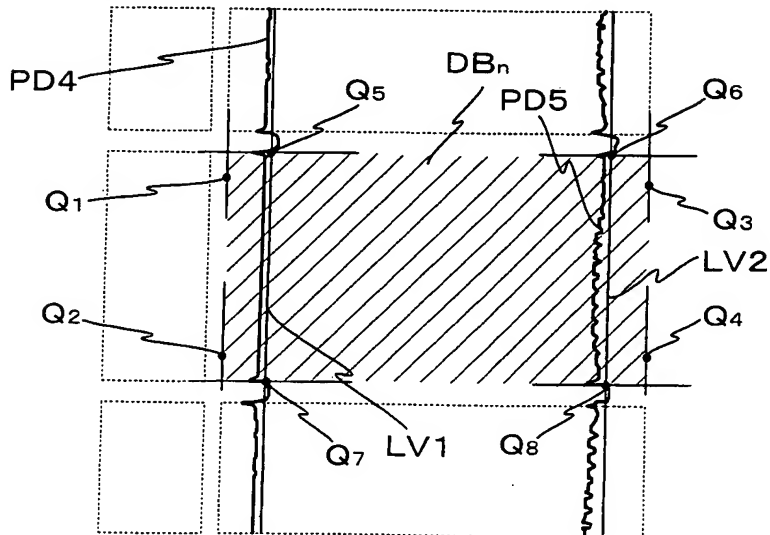
*Fig. 14A*



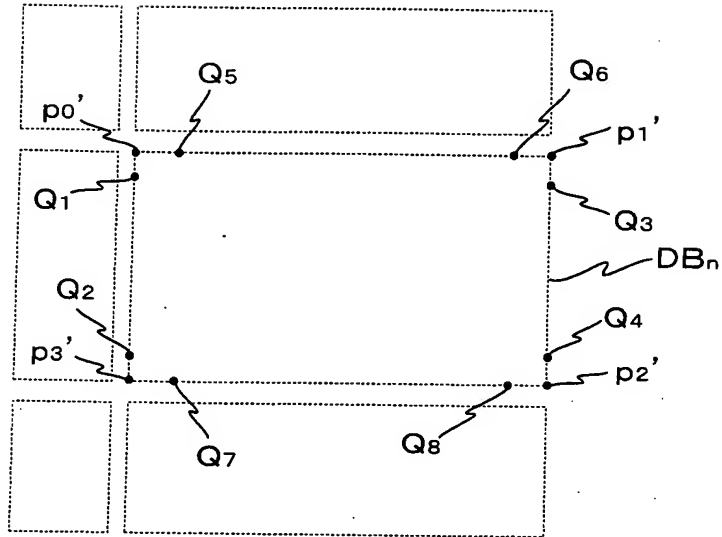
*Fig. 14B*



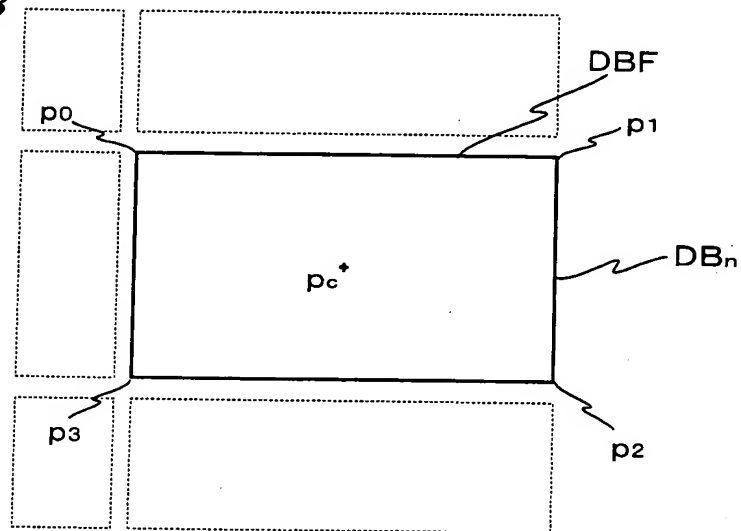
*Fig. 14C*



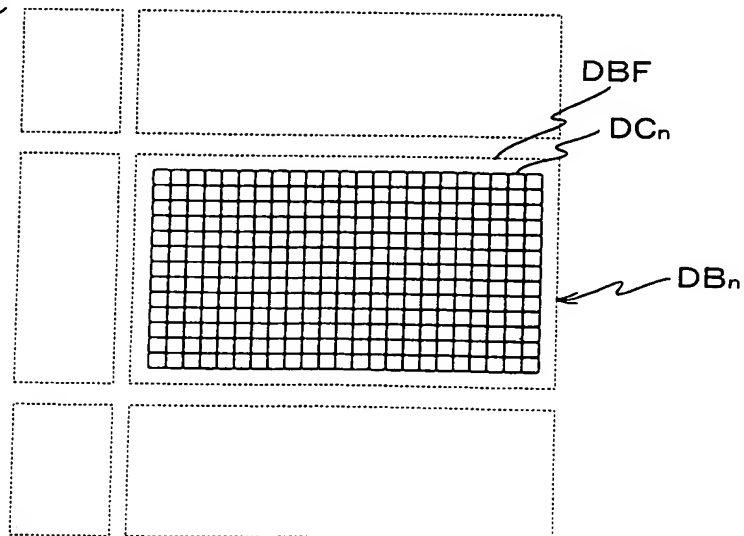
*Fig. 15A*



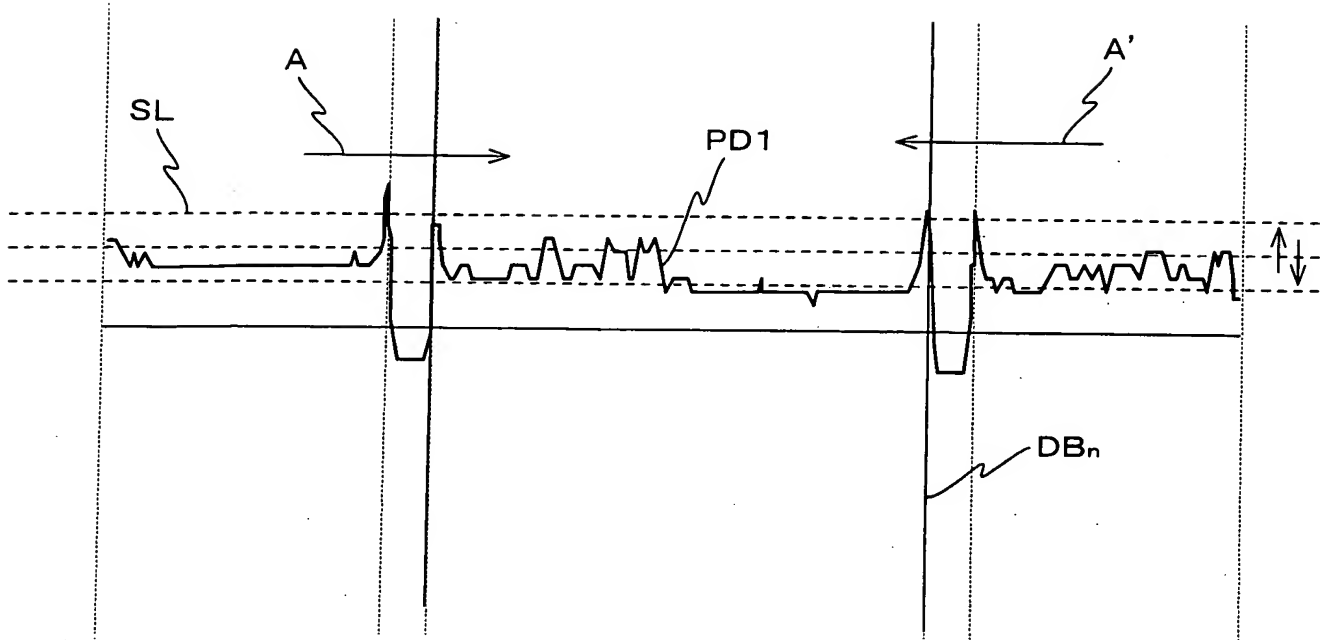
*Fig. 15B*



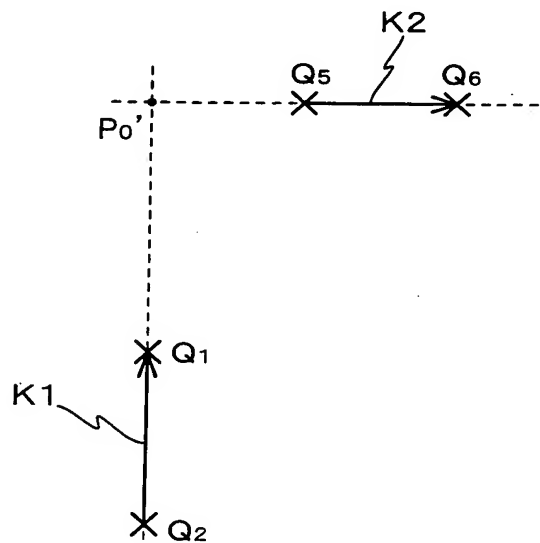
*Fig. 15C*



*Fig. 16*

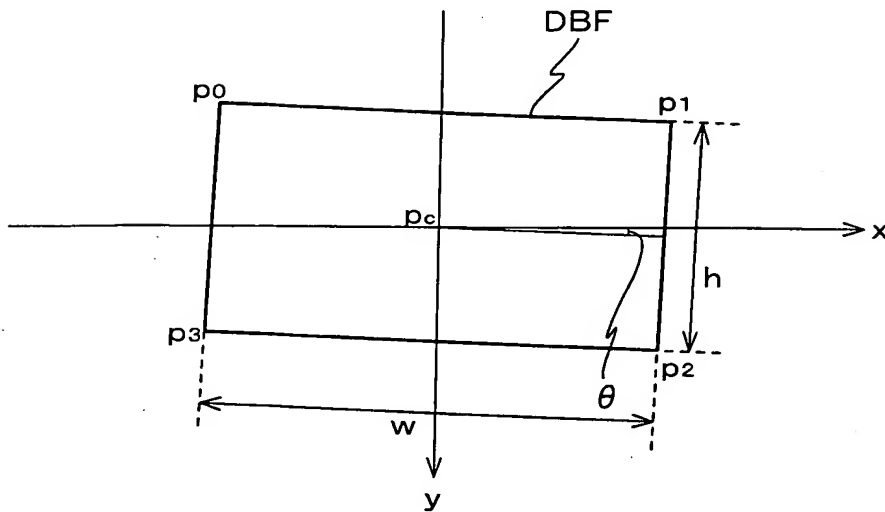


*Fig. 17*

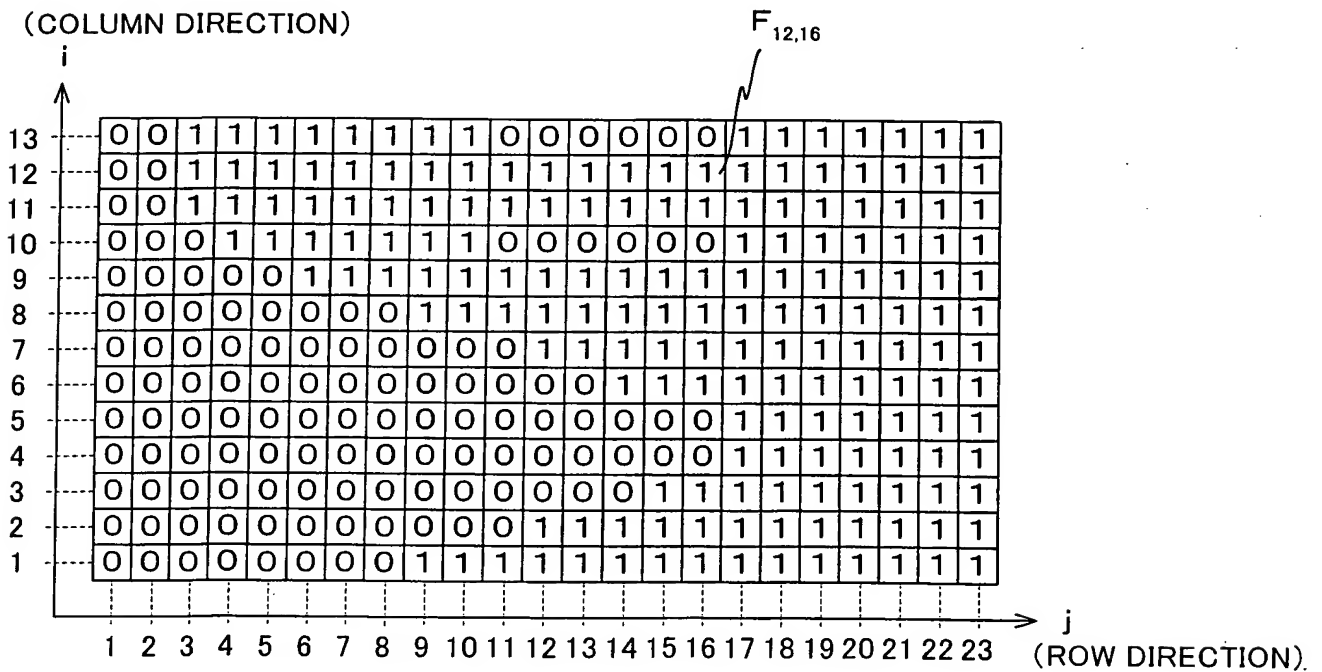




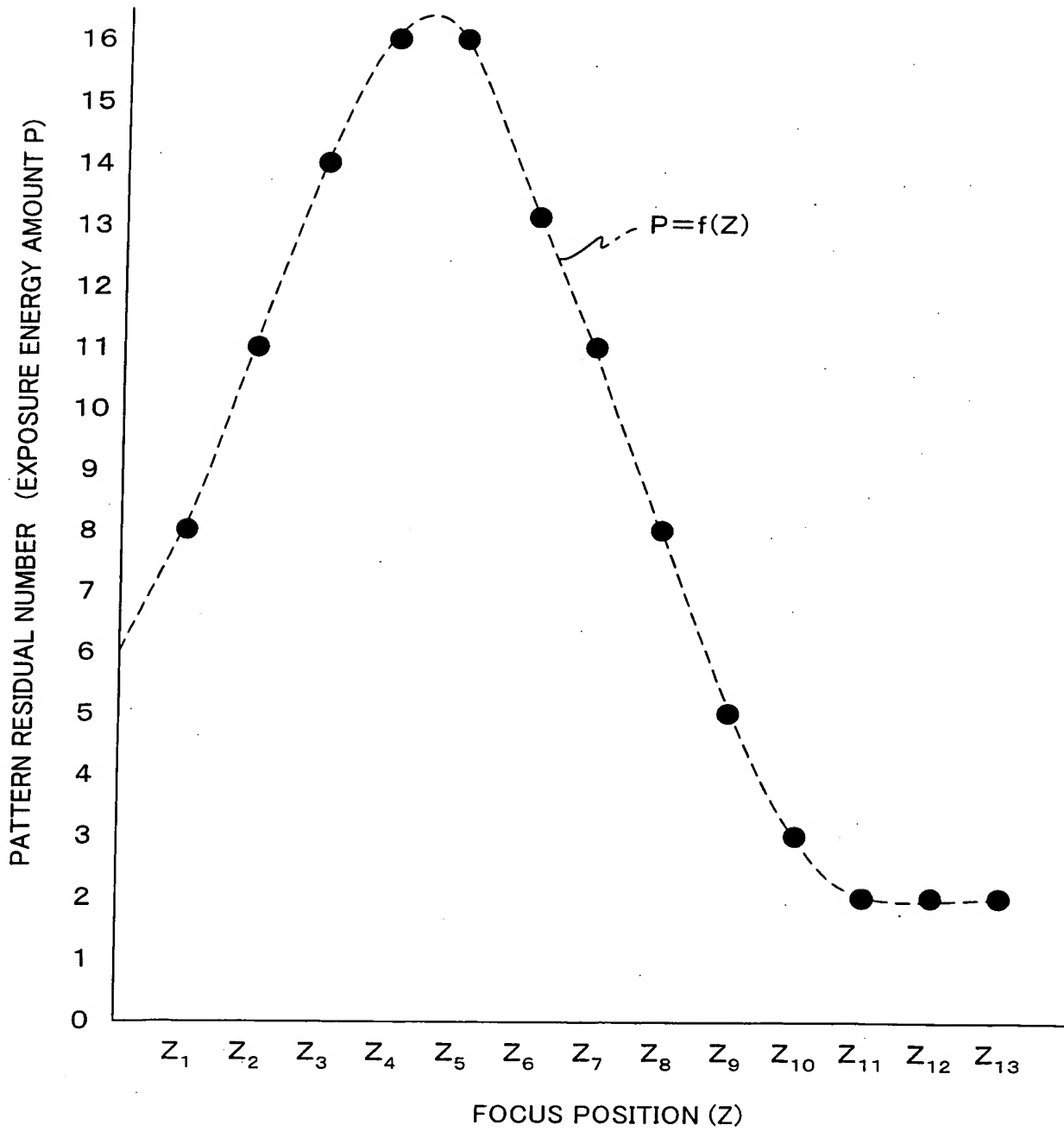
*Fig. 18*



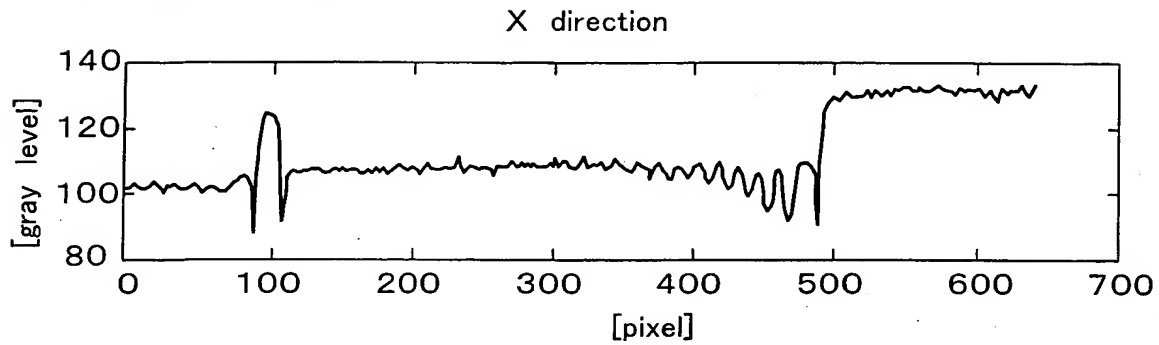
*Fig. 19*



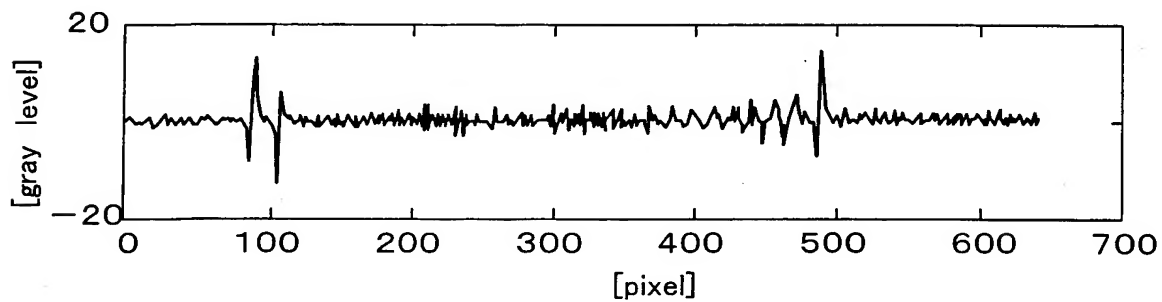
*Fig. 20*



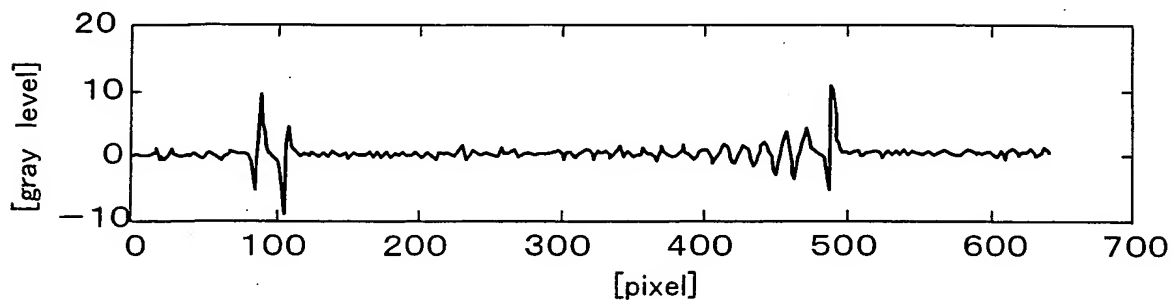
*Fig. 21A*



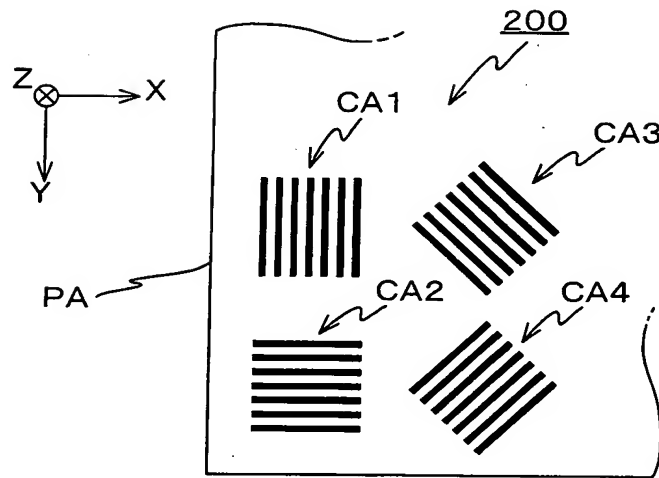
*Fig. 21B*



*Fig. 21C*



*Fig. 22*



*Fig. 23*

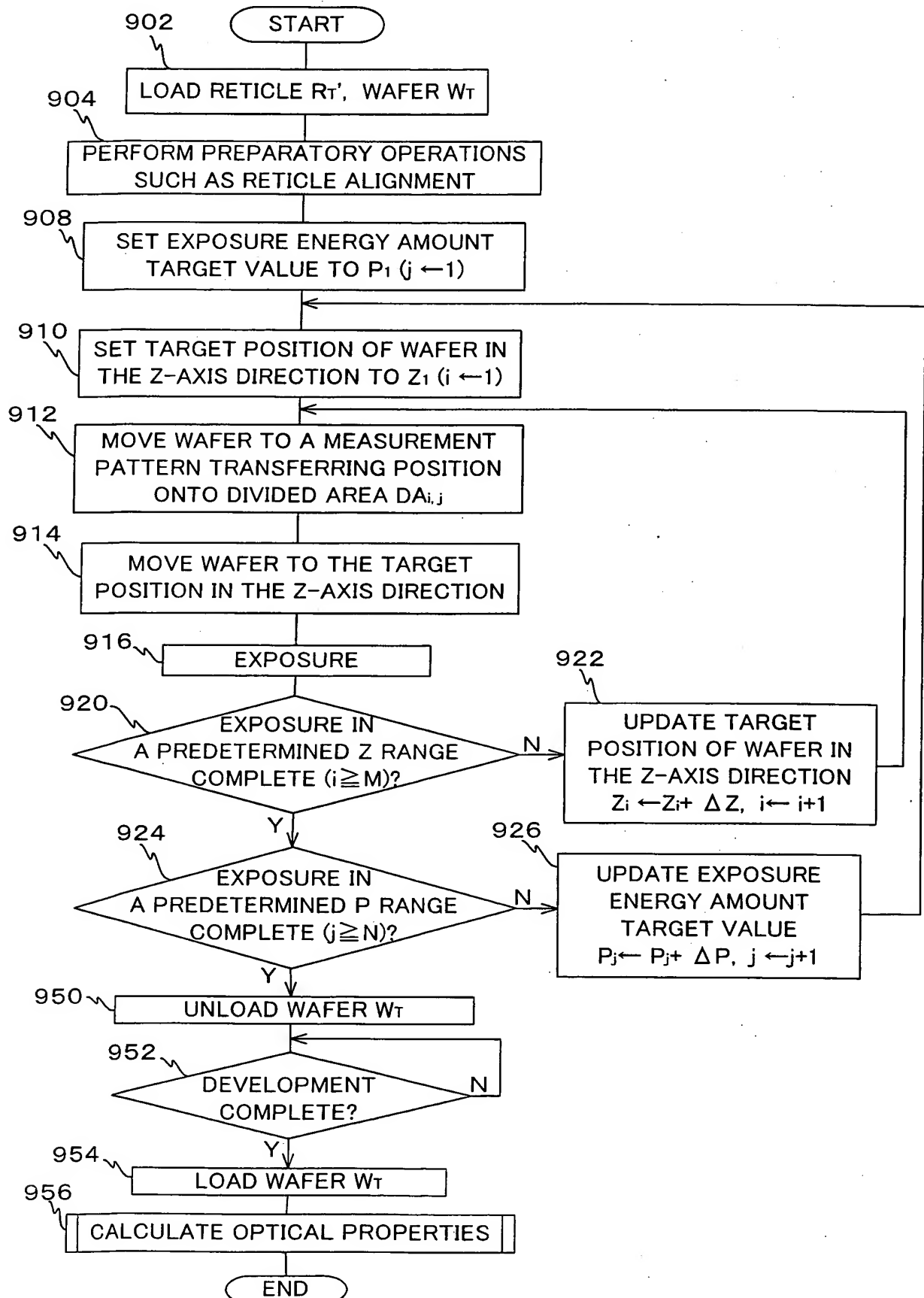
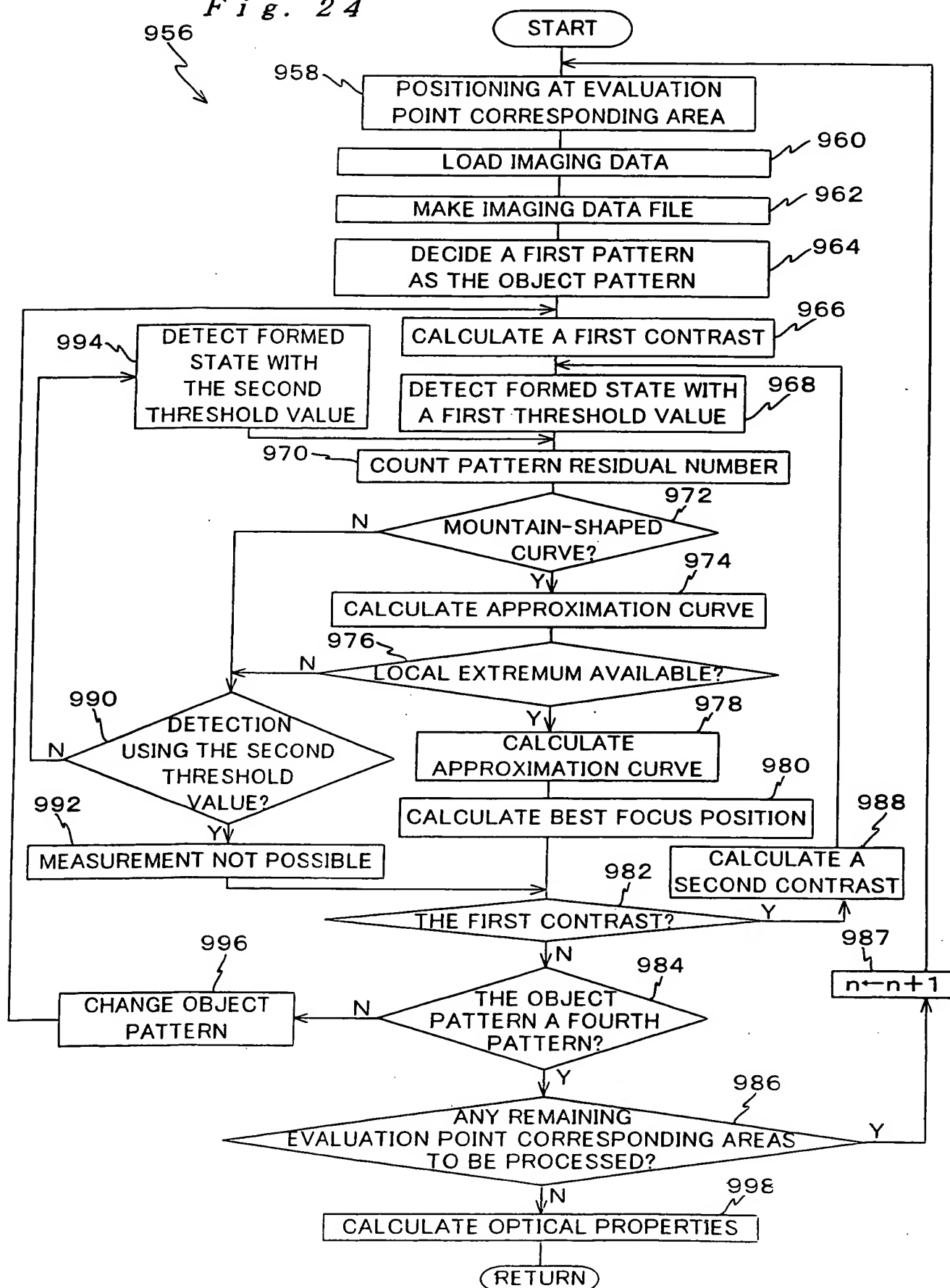
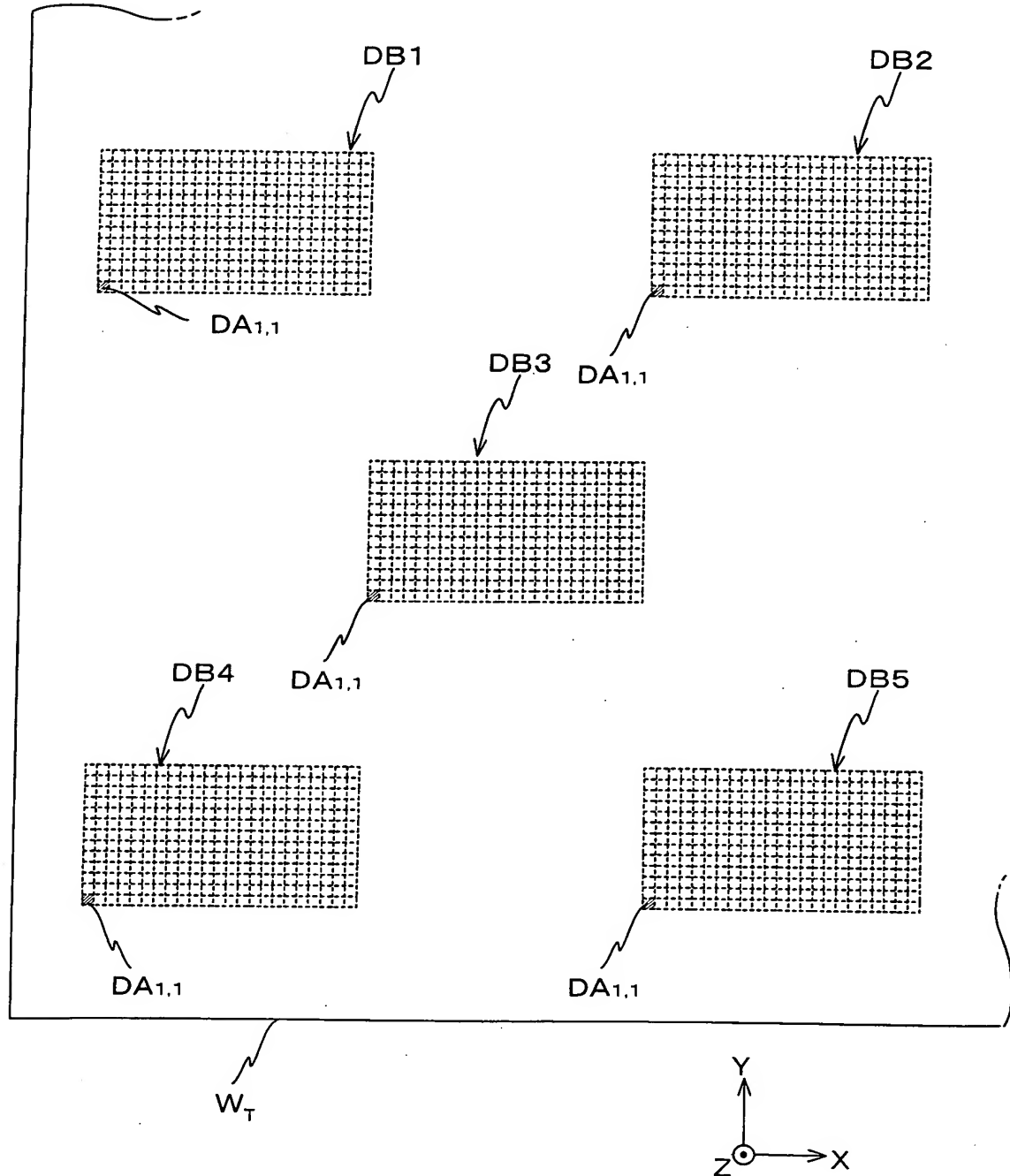


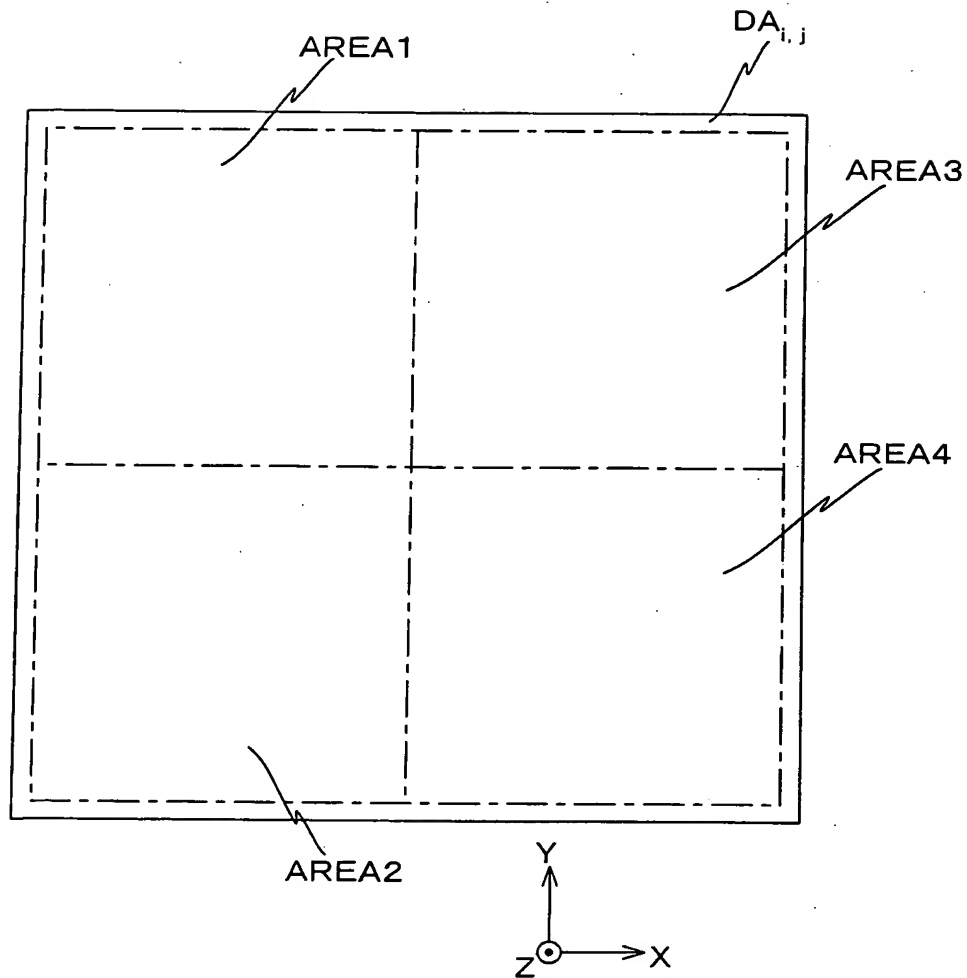
Fig. 24



*Fig. 25*

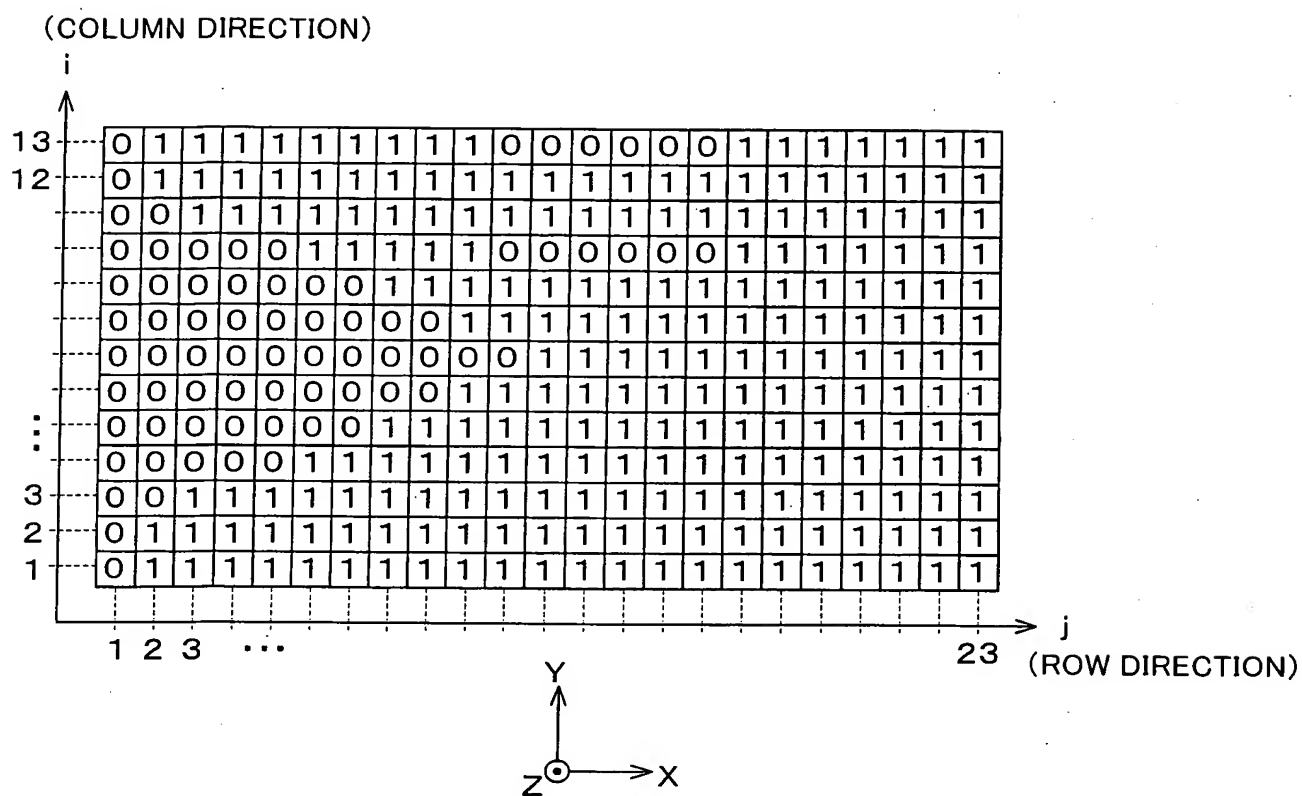


*Fig. 26*

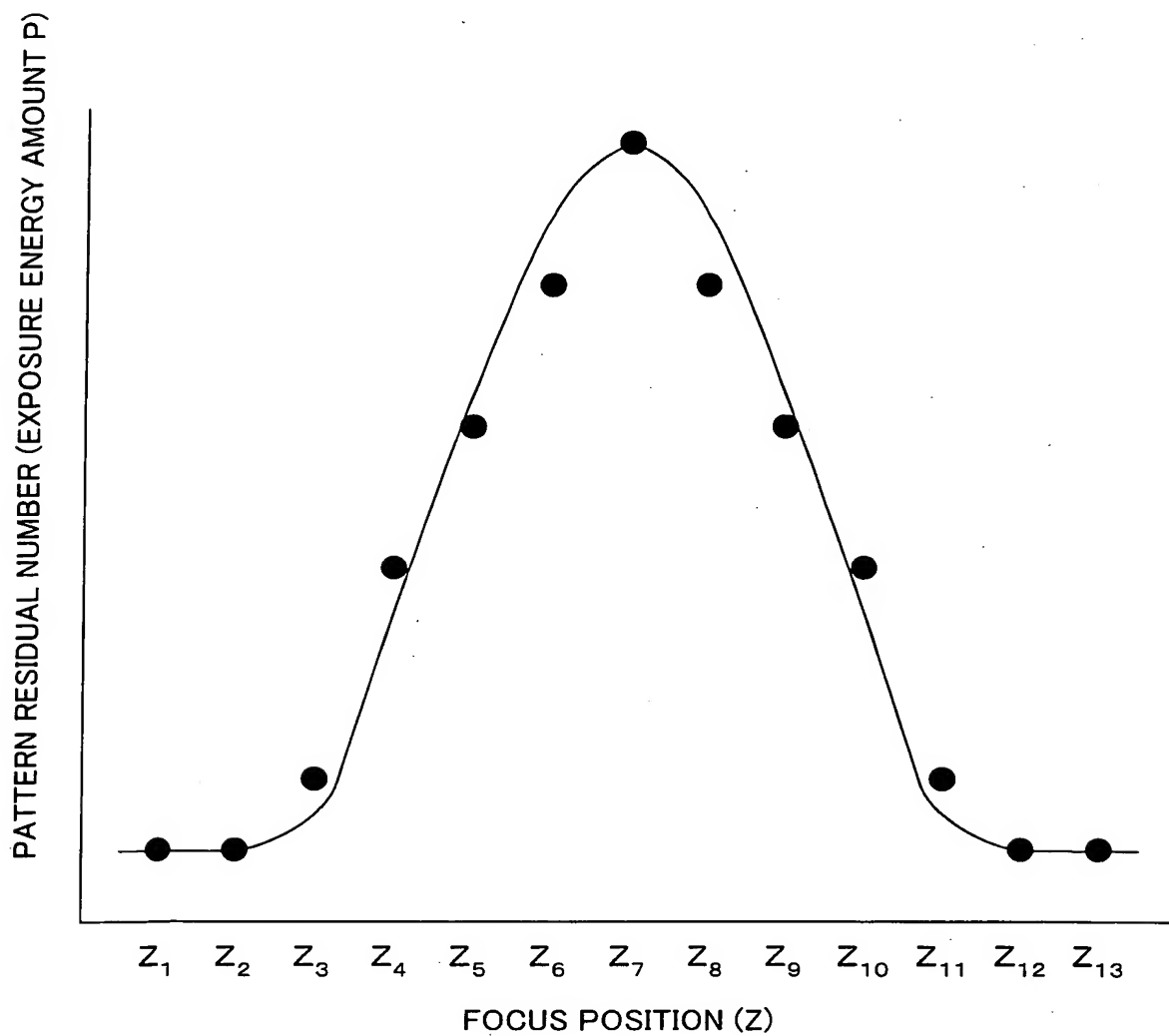




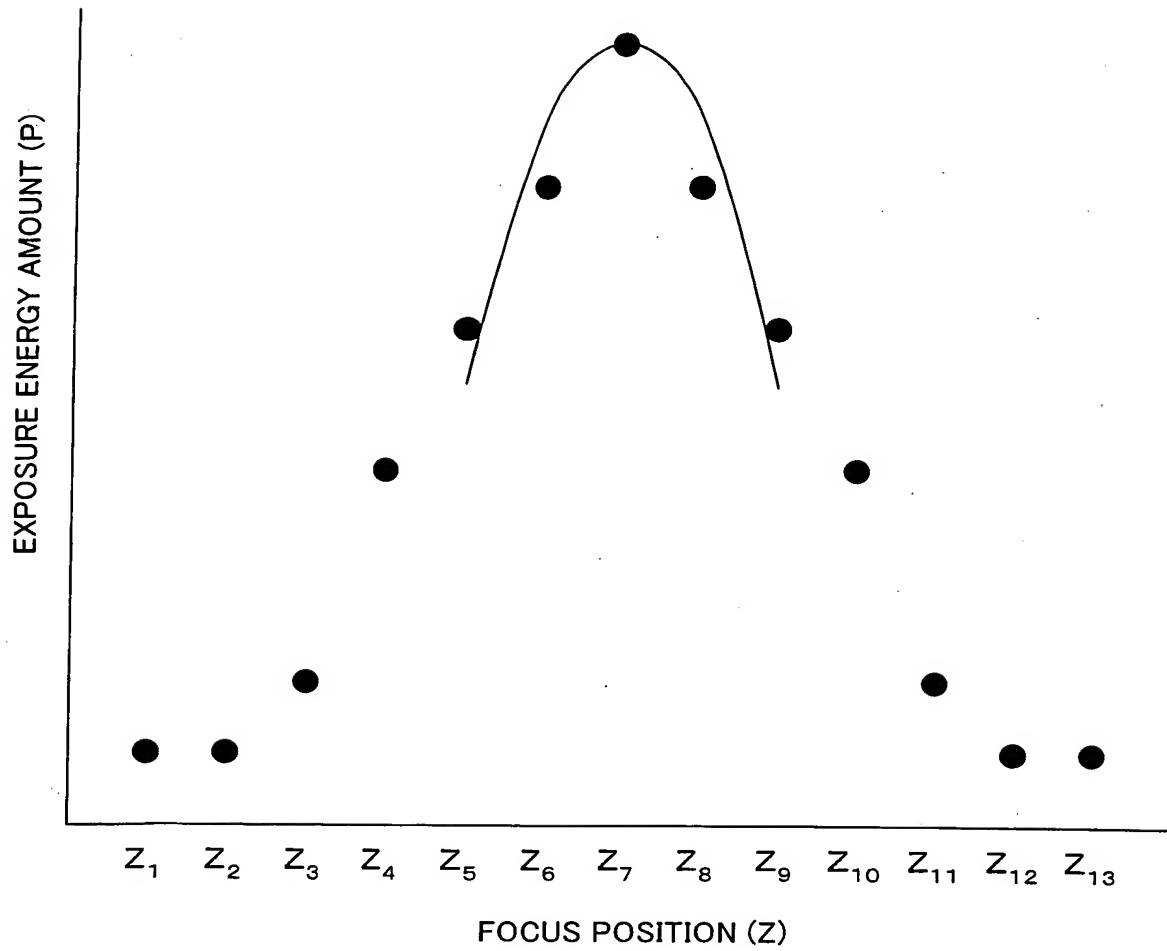
*Fig. 27*



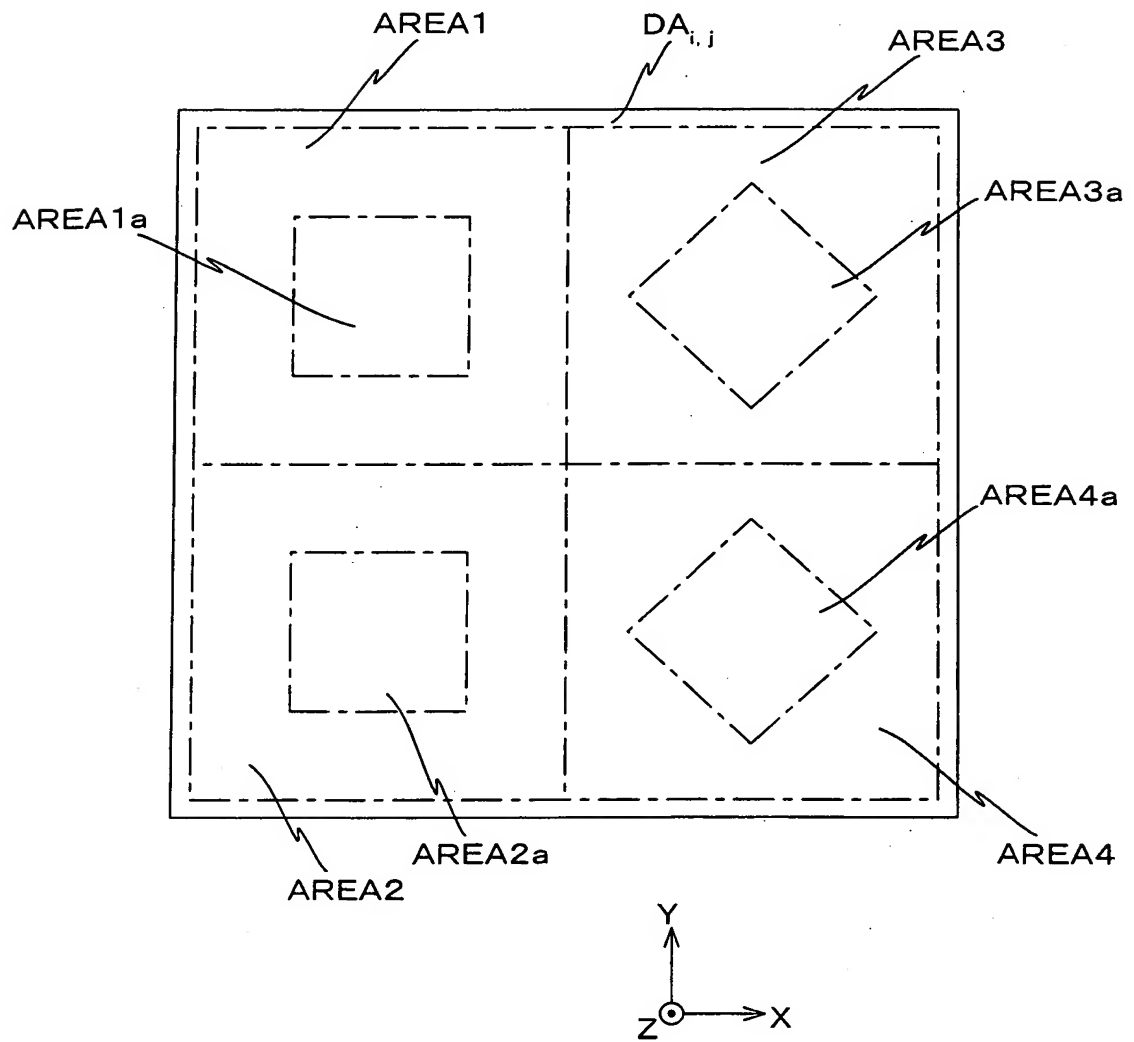
*Fig. 28*



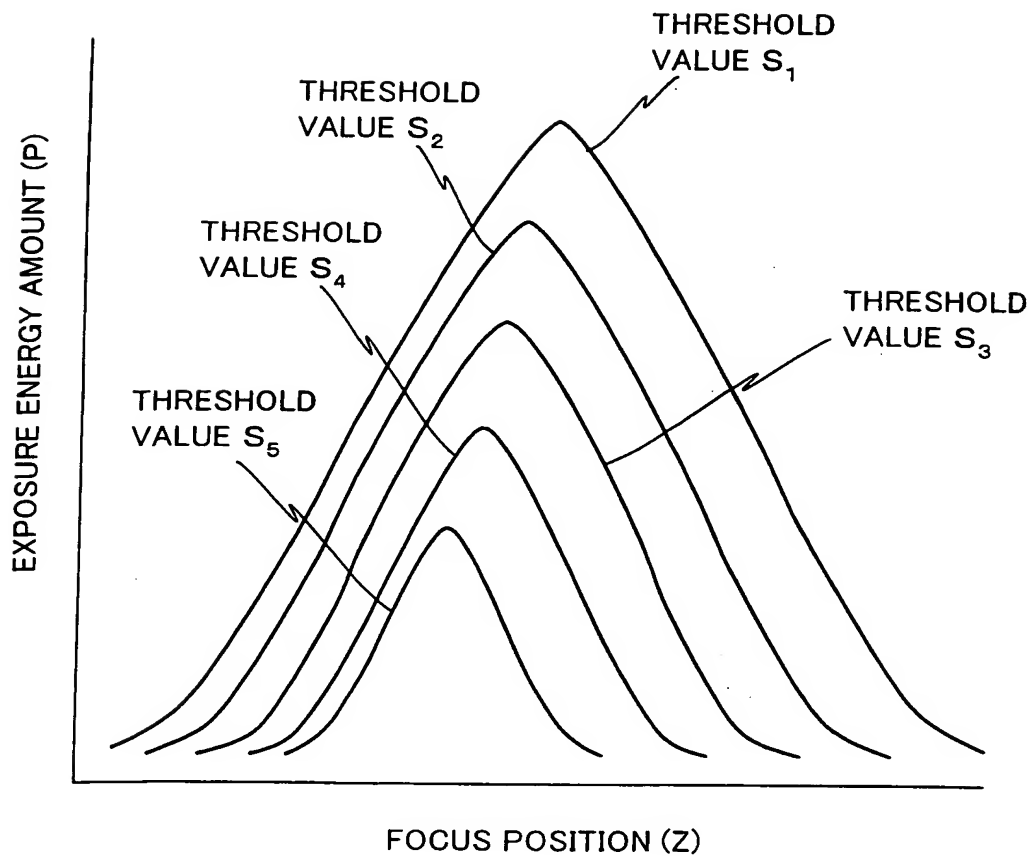
*Fig. 29*



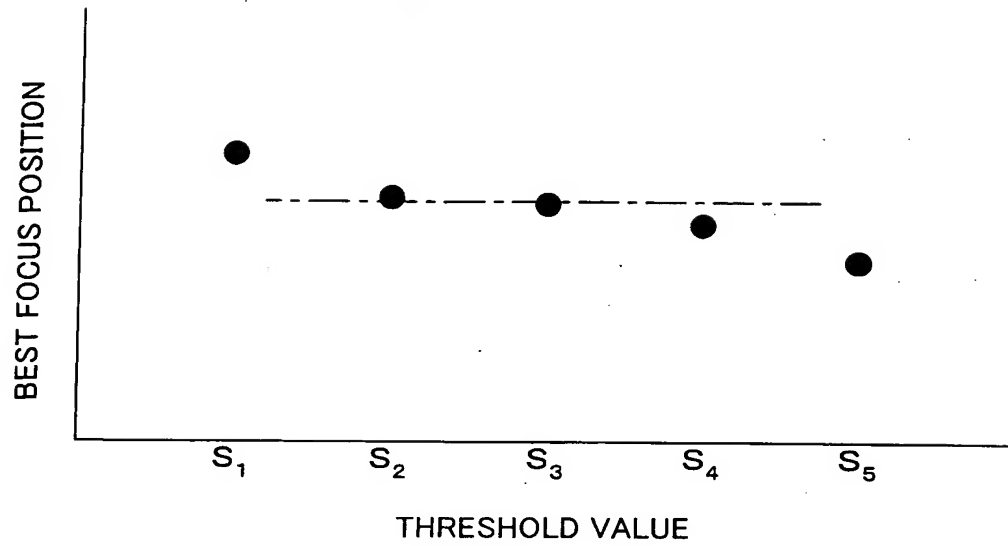
*Fig. 30*



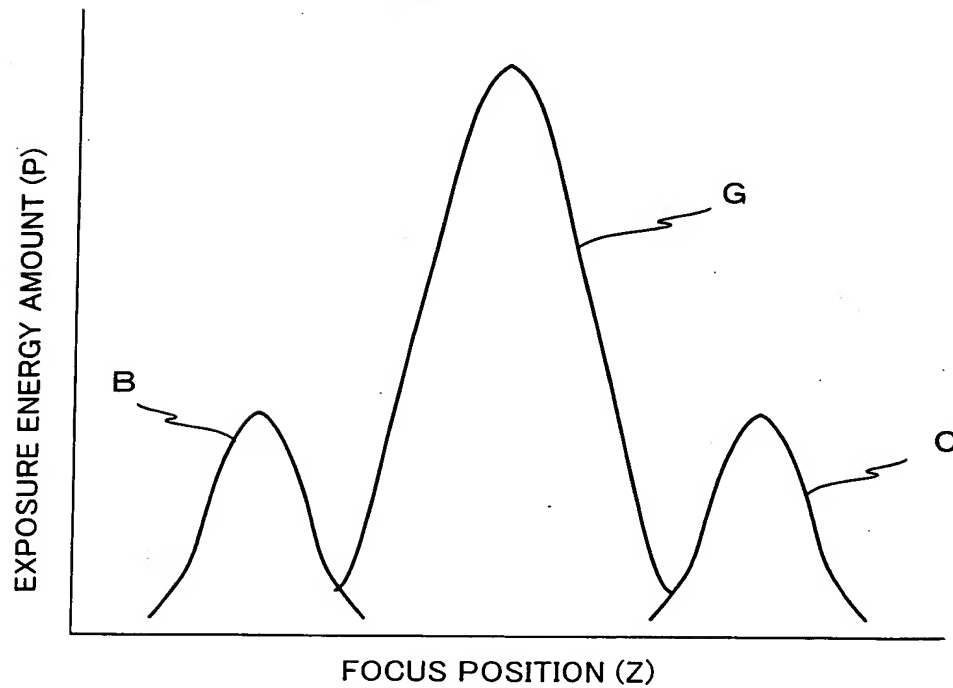
*Fig. 31*



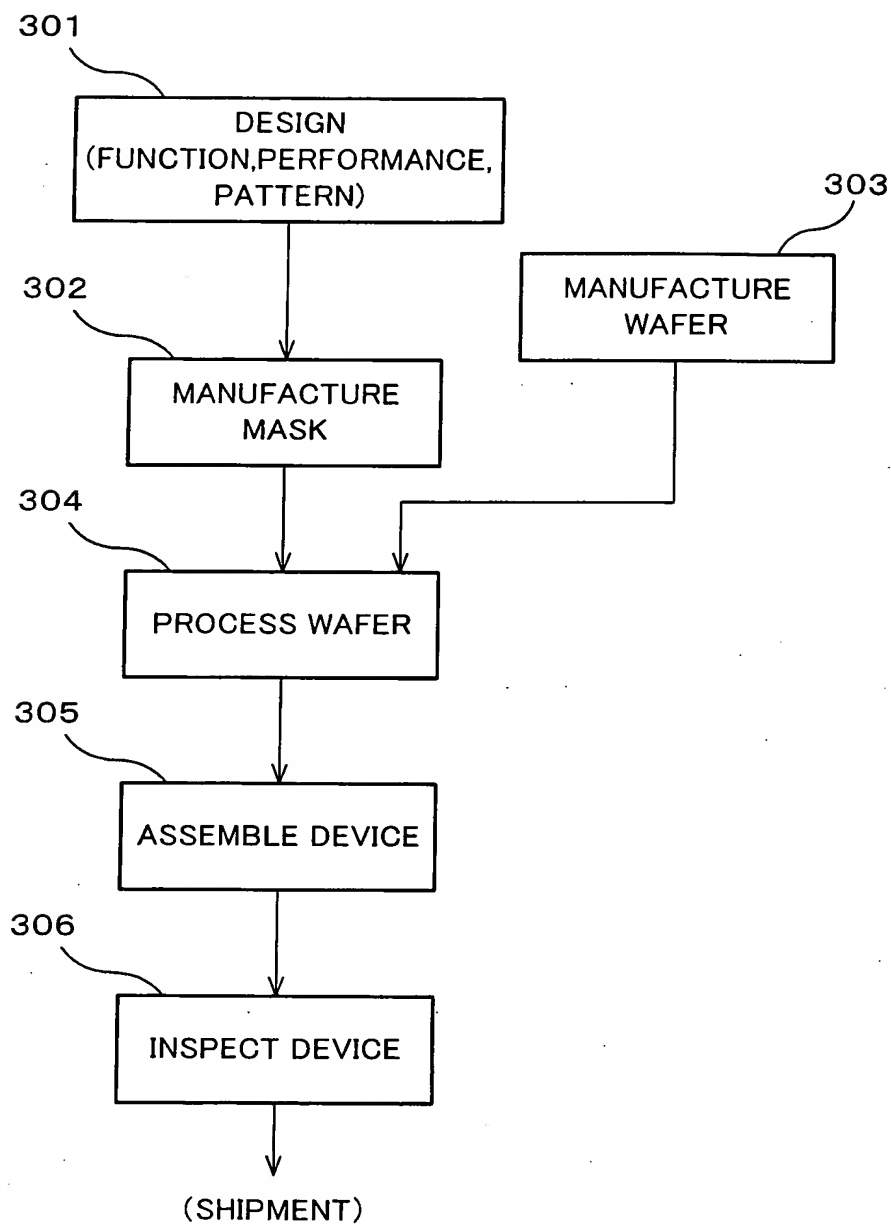
*Fig. 32*



*Fig. 33*



*Fig. 34*



*Fig. 35*

